



-1-

PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Vladislav Vashchenko et al.

Application No.: 10/823,455

Filed: April 13, 2004

For: LOW AREA LINEAR TIME-DRIVER
CIRCUIT

Confirmation No.: 4718

Group Art Unit: 2816

Examiner: Long T. Nguyen

**SUBMISSION OF REPLACEMENT
FORMAL DRAWINGS**

353 Sacramento Street, Suite 2200
San Francisco, CA 94111
(415) 772-4900

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 12, 2005.

Commissioner for Patents
MAIL STOP PGPUB
P.O. Box 1450
Alexandria, VA 22313-1450

STALLMAN & POLLOCK LLP

Dated: 10/12/05

By: 

Lana T. Brenner

Sir:

Applicant encloses three (3) sheets of formal drawings corresponding identically to the informal drawings filed with the above-identified U.S. patent application. Applicant requests that the formal drawings be substituted for the original-filed informal drawings.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: October 12, 2005

By: 

Michael J. Pollock

Reg. No. 29,098

Attorneys for Applicant(s)